



JLE33ULS3-2

1-Line Uni-directional TVS Diode

Jialan-Microelectronics

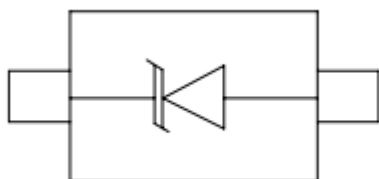
Description

The JLE33ULS3-2 is an uni-directional TVS diode, utilizing leading monolithic silicon technology to provide fast response time and ultra low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive data and power line. The JLE33ULS3 -2 complies with the IEC 61000-4-2 (ESD) with $\pm 30kV$ air and $\pm 30kV$ contact discharge. It is assembled into an ultra-small SOD-323 lead-free package. The small size and high ESD surge protecton make JLE33ULS3 -2 an ideal choice to protect cell phone, digital cameras, audio players and many other portable applications.

Features

- * 100W peak pulse power (8/20 μ s)
- * Low leakage:nA level
- * Operating voltage: 3.3V
- * Low clamping voltage
- * One power line protects
- * Complies with following standards:
 - IEC 61000-4-2 (ESD) immunity test
 - Air discharge: $\pm 30kV$
 - Contact discharge: $\pm 30kV$
 - IEC61000-4-5 (Lightning) 10A (8/20 μ s)
- * RoHS Compliant
- * Package: SOD-323

Circuit Diagram

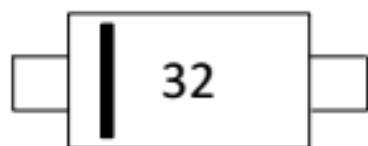


Circuit and Pin Schematic

Applications

- * Cellular Handsets and Accessories
- * Personal Digital Assistants
- * Notebooks and Handhelds
- * Portable Instrumentation
- * Digital Cameras
- * Peripherals
- * Audio Players
- * Keypads, Side Keys, LCD Displays

Marking Diagram



Transparent top view

32:Device Marking Code

Ordering Information

Part Number	Packaging	Reel Size
JLE33ULS3-2	3000/Tape & Reel	7 inch



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Absolute Maximum Ratings ($T_A=25^\circ\text{C}$ unless otherwise specified)

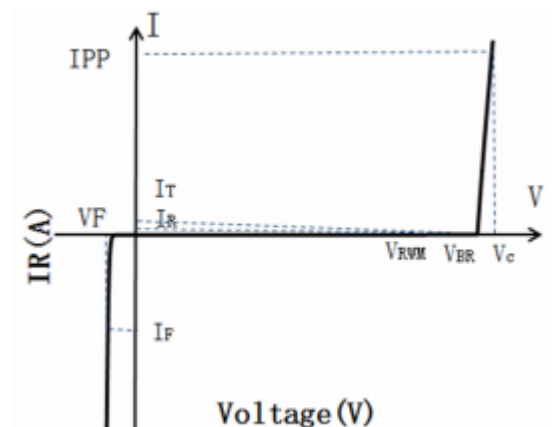
Parameter	Symbol	Value	Unit
Peak Pulse Power (8/20 μs)	Ppk	100	W
Peak Pulse Current (8/20 μs)	IPP	10	A
ESD per IEC 61000-4-2 (Air)	VESD	± 30	kV
ESD per IEC 61000-4-2 (Contact)		± 30	
Operating Temperature Range	TJ	-55to +125	$^\circ\text{C}$
Storage Temperature Range	Tstg	-55 to +150	$^\circ\text{C}$

Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Reverse Working Voltage	V_{RWM}				3.3	V
Breakdown Voltage	V_{BR}	$I_T = 1\text{mA}$	3.5			V
Reverse Leakage Current	I_R	$V_{RWM} = 3.3\text{V}$			0.2	μA
Clamping Voltage	V_C	$I_{PP} = 1\text{A}$ (8 x 20 μs pulse)			7	V
Clamping Voltage	V_C	$I_{PP} = 10\text{A}$ (8 x 20 μs pulse)			10	V
Junction Capacitance	C_J	$V_R = 0\text{V}$, $f = 1\text{MHz}$		60		pF

Portion Electronics Parameter

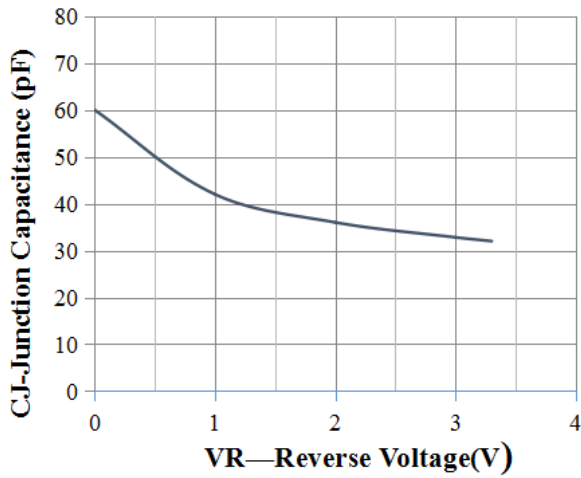
Symbol	Parameter
I_T	Test Current
I_{PP}	Maximum Reverse Peak Pulse Current
V_C	Clamping Voltage @ I_C



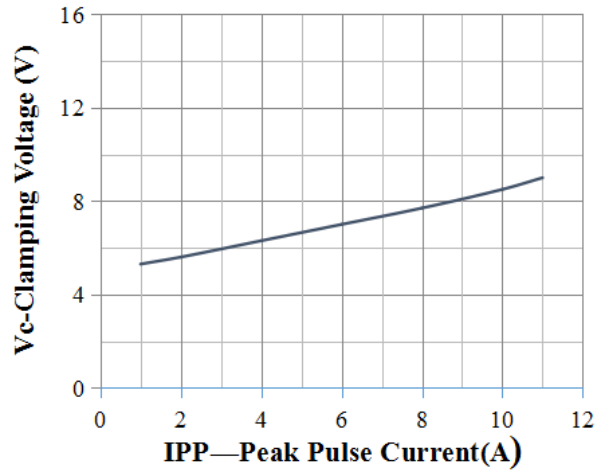


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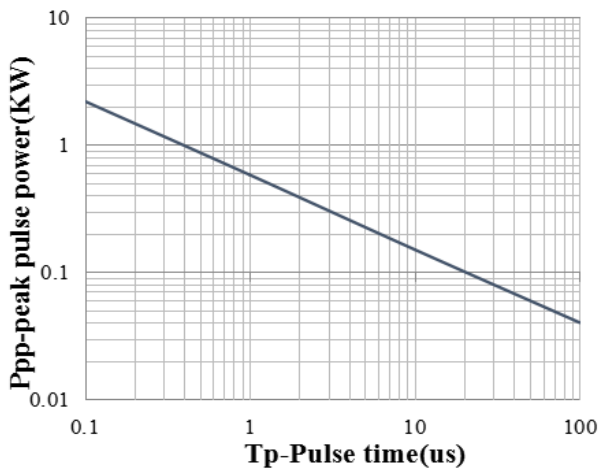
Typical Performance Characteristics ($T_A=25^\circ\text{C}$ unless otherwise Specified)



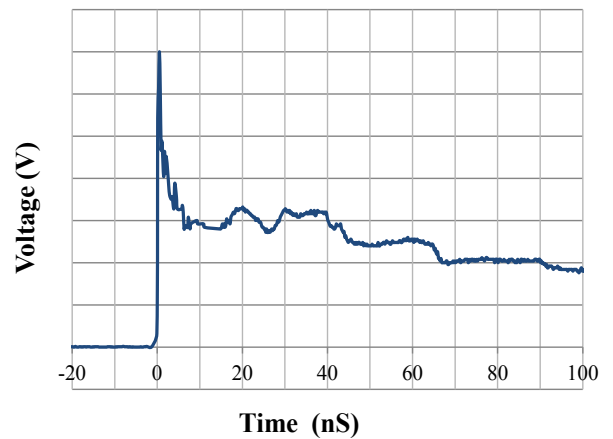
Junction Capacitance vs. Reverse Voltage



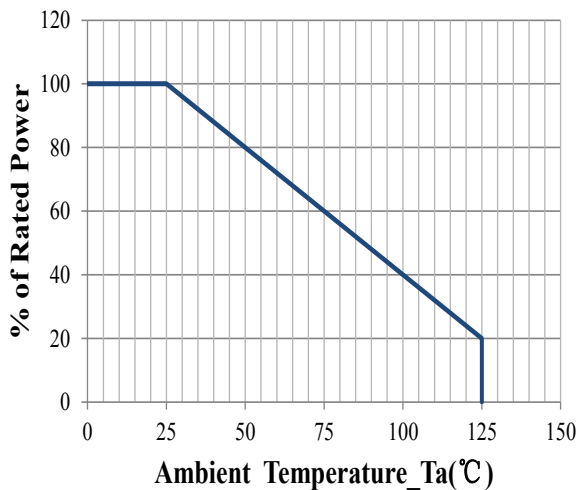
Clamping Voltage vs. Peak Pulse Current



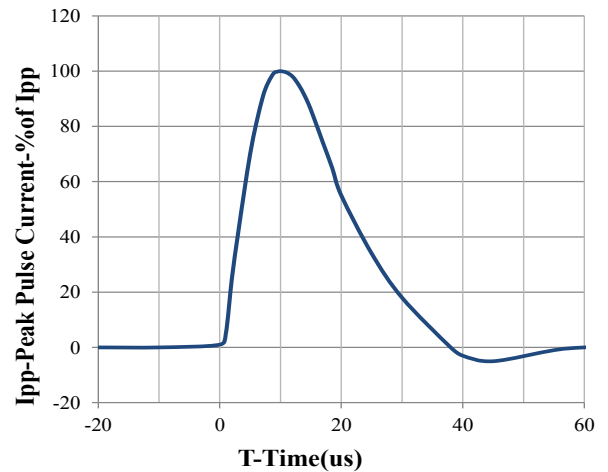
Peak Pulse Power vs. Pulse Time



IEC61000-4-2 Pulse Waveform



Power Derating Curve

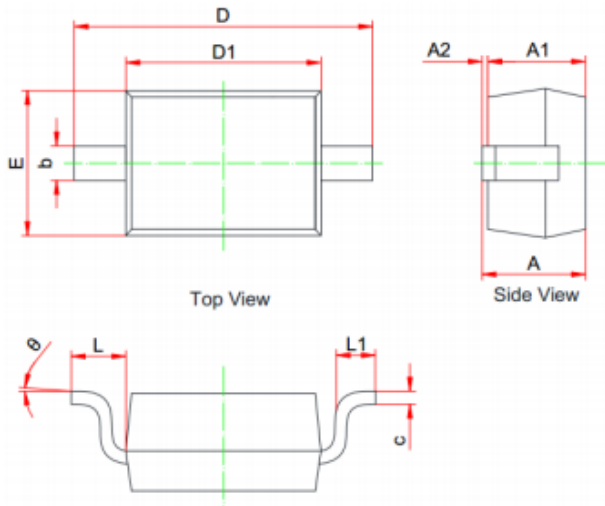


8 X 20us Pulse Waveform



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SOD-323 Package Outline Drawing (Dimensions in millimeters)



	MILLIMETERS		
	MIN	NOM	MAX
A	0.800	--	1.100
A1	0.800	--	0.900
A2	0.000	--	0.100
b	0.250	--	0.400
c	0.080	--	0.177
D1	1.600	1.700	1.800
D	2.300	--	2.800
E	1.150	--	1.400
L	0.475REF		
L1	0.100	--	0.500
θ	0°	--	8°

Suggested Land Pattern



Unit: mm

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